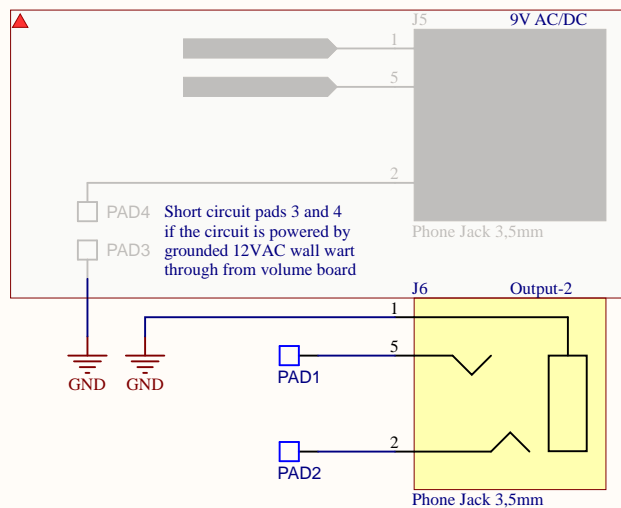
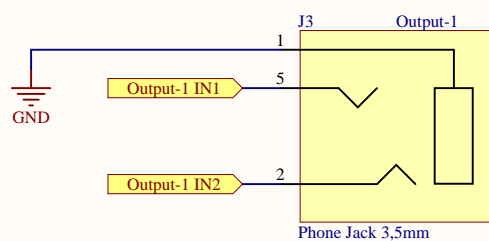


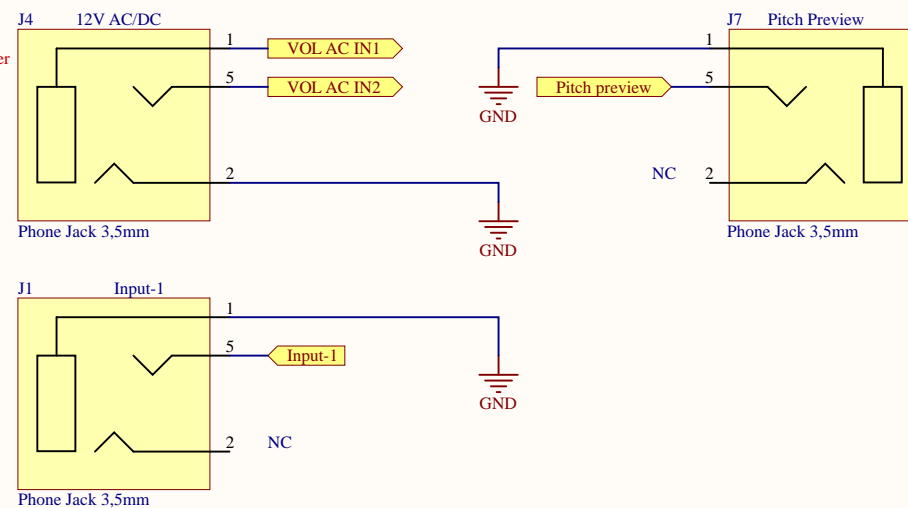
PITCH BOARD



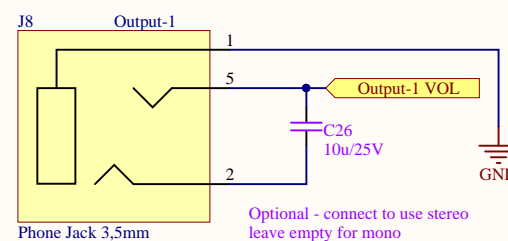
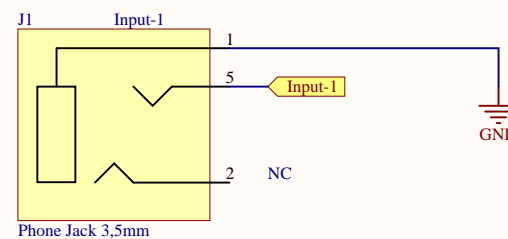
Connection for an accessory Output from NE555 or unmodified sound before transformer can be fed here



VOLUME BOARD



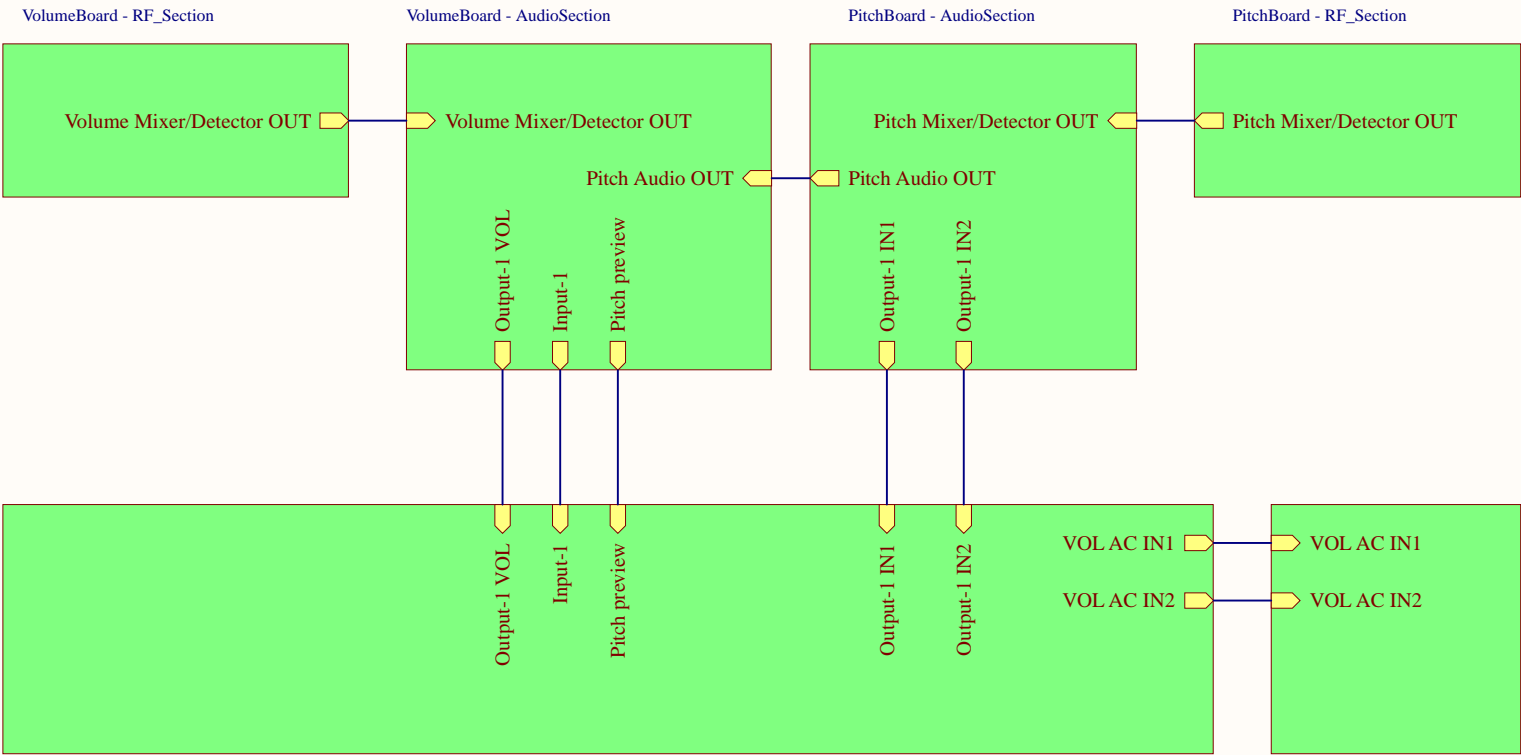
This connector should be replaced by a standard power connector. TRS jack is not really appropriate for supplying power



Title		
Phoenix Theremin 2020 - Front panel		
Size	Number	Revision
A4	Humorous words of wisdom ...	1.07.19A
Date:	21. 01. 2019	Sheet of
File:	H:\FAKS\...\FrontPanel.SchDoc	Drawn By:

Volume board

Pitch board



Front panel connections

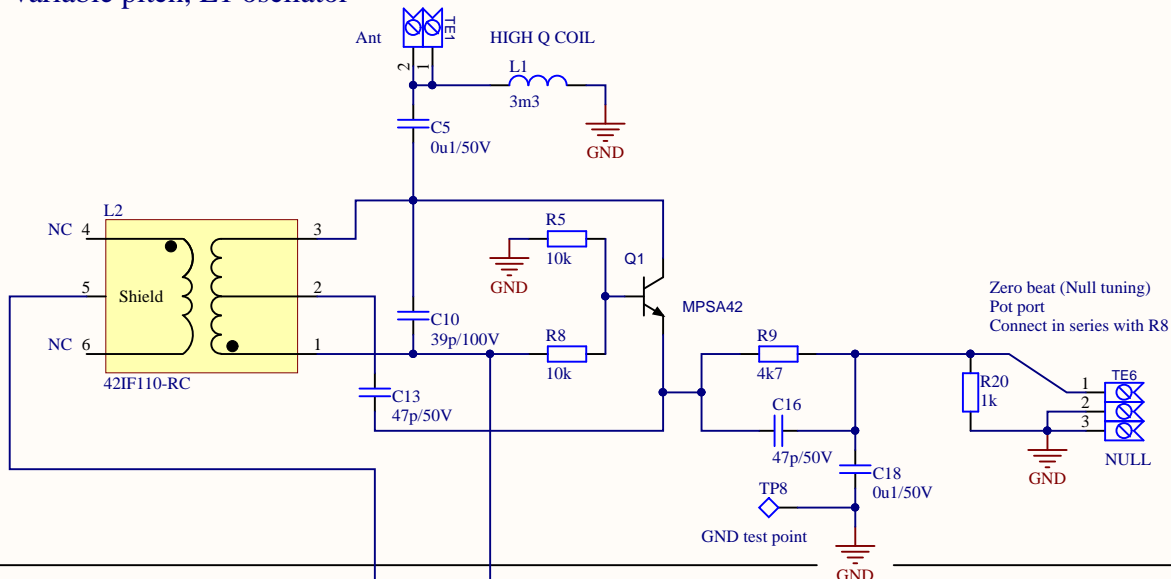
Power supply

Power ports for pitch board are not mandatory and are only needed in the case of pitch board being used separately, without the volume board. In that case the pitch board needs it's own power supply.

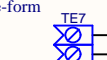
This field will be edited once I figure out how to change the layout of the black area

Title			
Size	Number		Revision
A4			
Date:	21. 01. 2019		Sheet of
File:	H:\FAKS\...\Phoenix.SchDoc		Drawn By:

Variable pitch, L1 oscillator

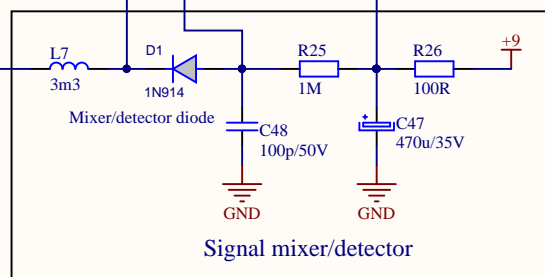


A 47k resistor can be mounted on the terminal from ports A to C or A to D to shape the signal wave-form



Terminal 1

Pitch Mixer/Detector OUT

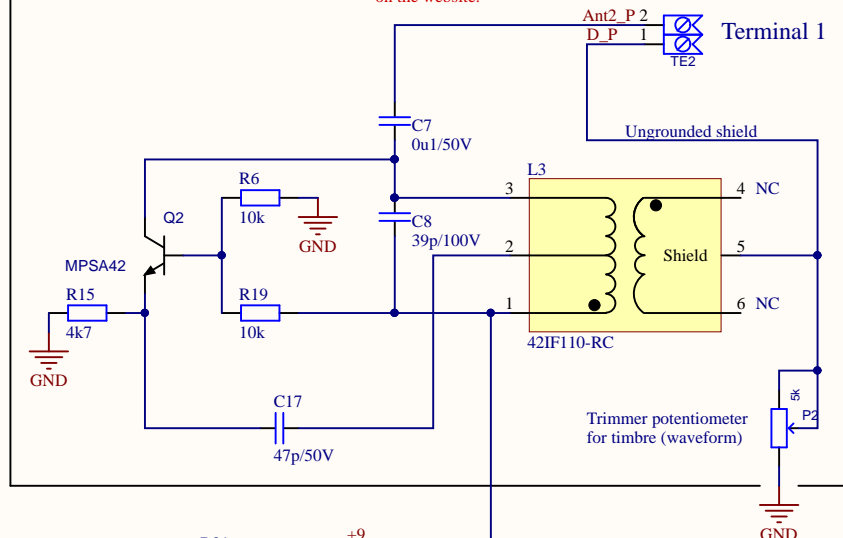


Fixed pitch, L2 oscillator

$f = 900\text{KHz}$

How should I name the connector in the place of Ant2_P? This is the marking I see on the website.

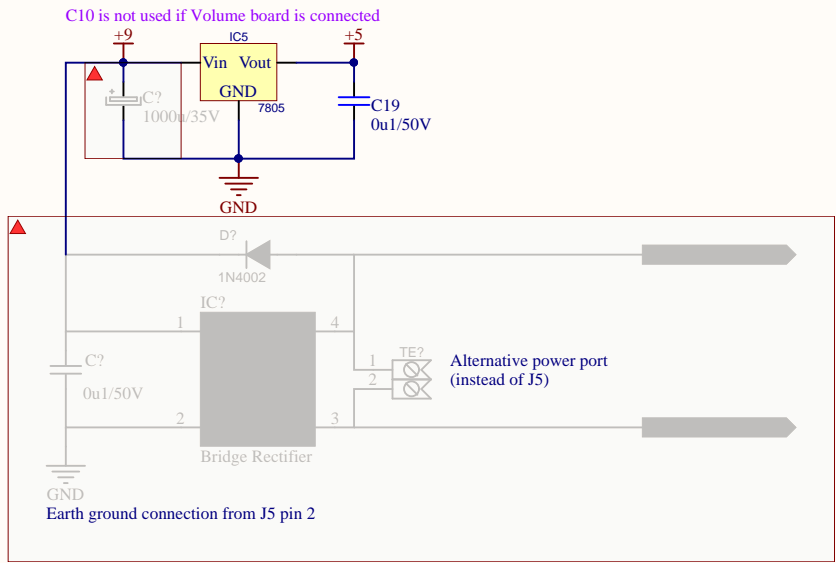
C-D is a port for panel mounted 2k potentiometer P5A used for signal waveform adjustment. P5A is mounted in parallel to P5



P2 and P3 are used to balance thermal drift they are not mandatory however and may be left out, as they will be bypassed by R9 and R27.

Title		
Phoenix Theremin 2020 - Pitch board, RF section		
Size	Number	Revision
A4	Theoretically speaking, ...	1.07.19A
Date:	21. 01. 2019	Sheet of
File:	H:\FAKS\...PitchBoard - RF_Section.Sch	Drawn By:

This part of the circuit is placed on the pitch board



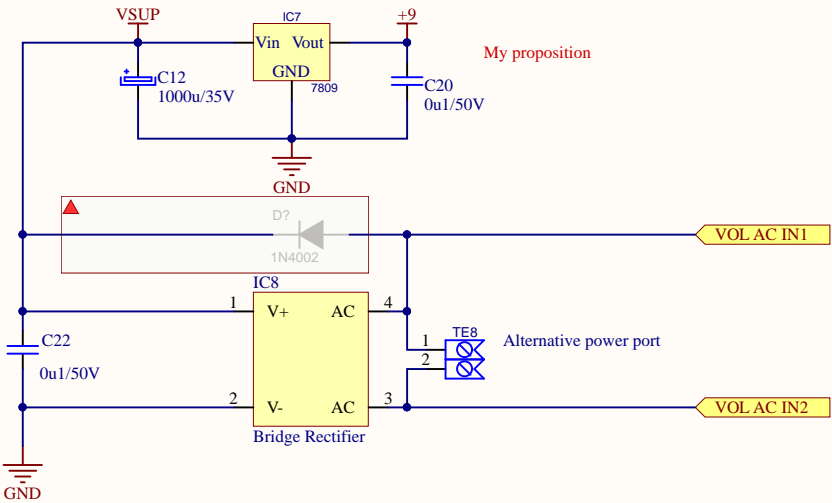
I will remove the greyed out areas.
I will however, make a separate schematic for the 2 board variant and leave the appropriate parts of the circuit on the schematic.

FIDUCIAL MARKS -
they play no role in how the circuit works whatsoever.
They are used if anyone intends to send gerber files to a company that produces printed circuit boards so the machines that etch the board (and possibly place the parts) can orient what is their position over the board.

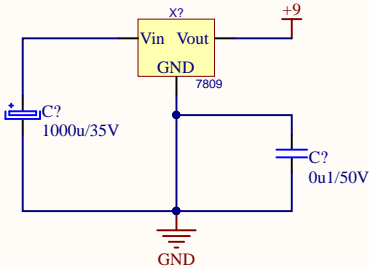


They are seen in the corners of the PCB, placed on top (red) side.

This part of the circuit is placed on the volume board



I still see this and it doesn't look right to me.
Did you forgot to reupload a new image
Or is this intended?



Title			
Phoenix Theremin 2020 - Power supply			
Size	Number	Revision	
A4	... practice and theory ...	1.07.19A	
Date:	21. 01. 2019	Sheet	of
File:	H:\FAKS\...\PowerSupply.SchDoc	Drawn By:	

1

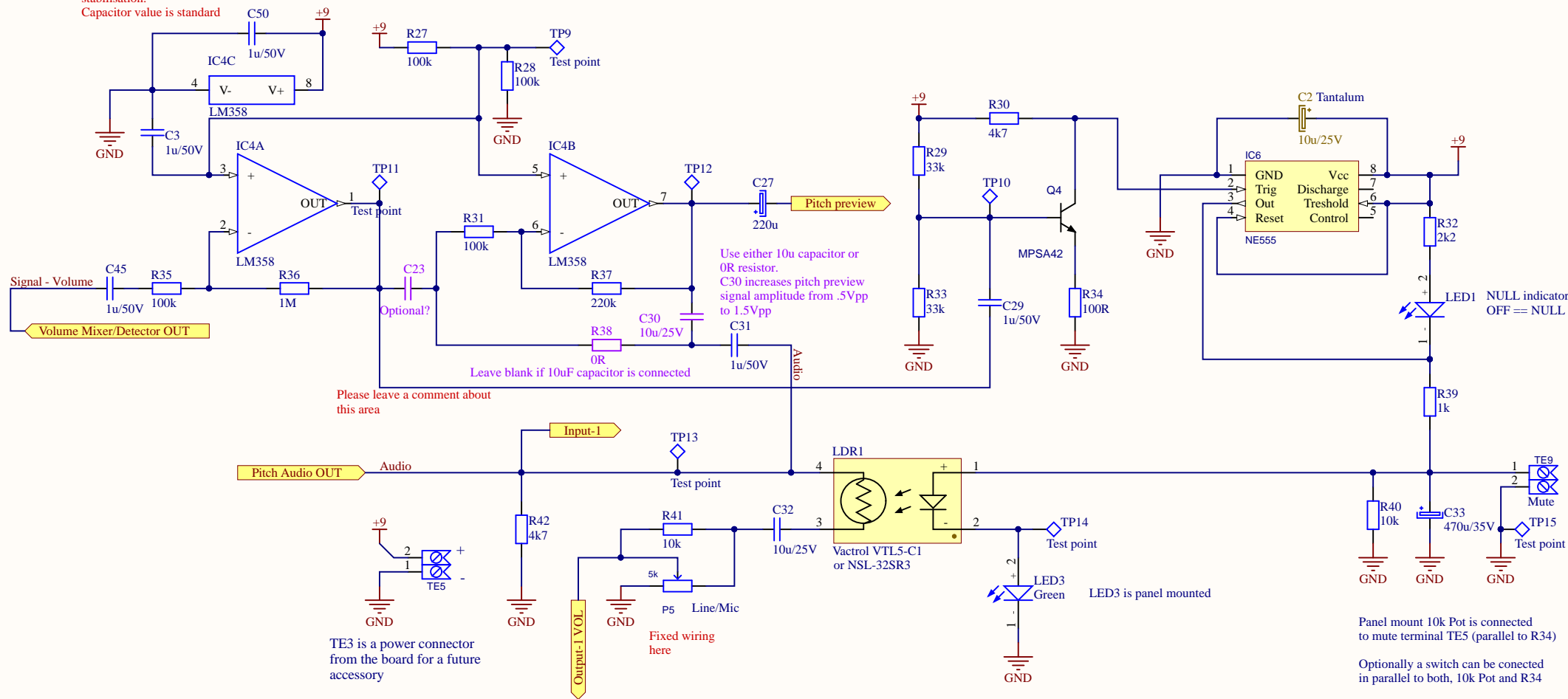
2

3

4

VOLUME BOARD

ADDED - 1u/50V Blocking capacitor for OPAMP voltage stabilisation.
Capacitor value is standard



VOLUME BOARD

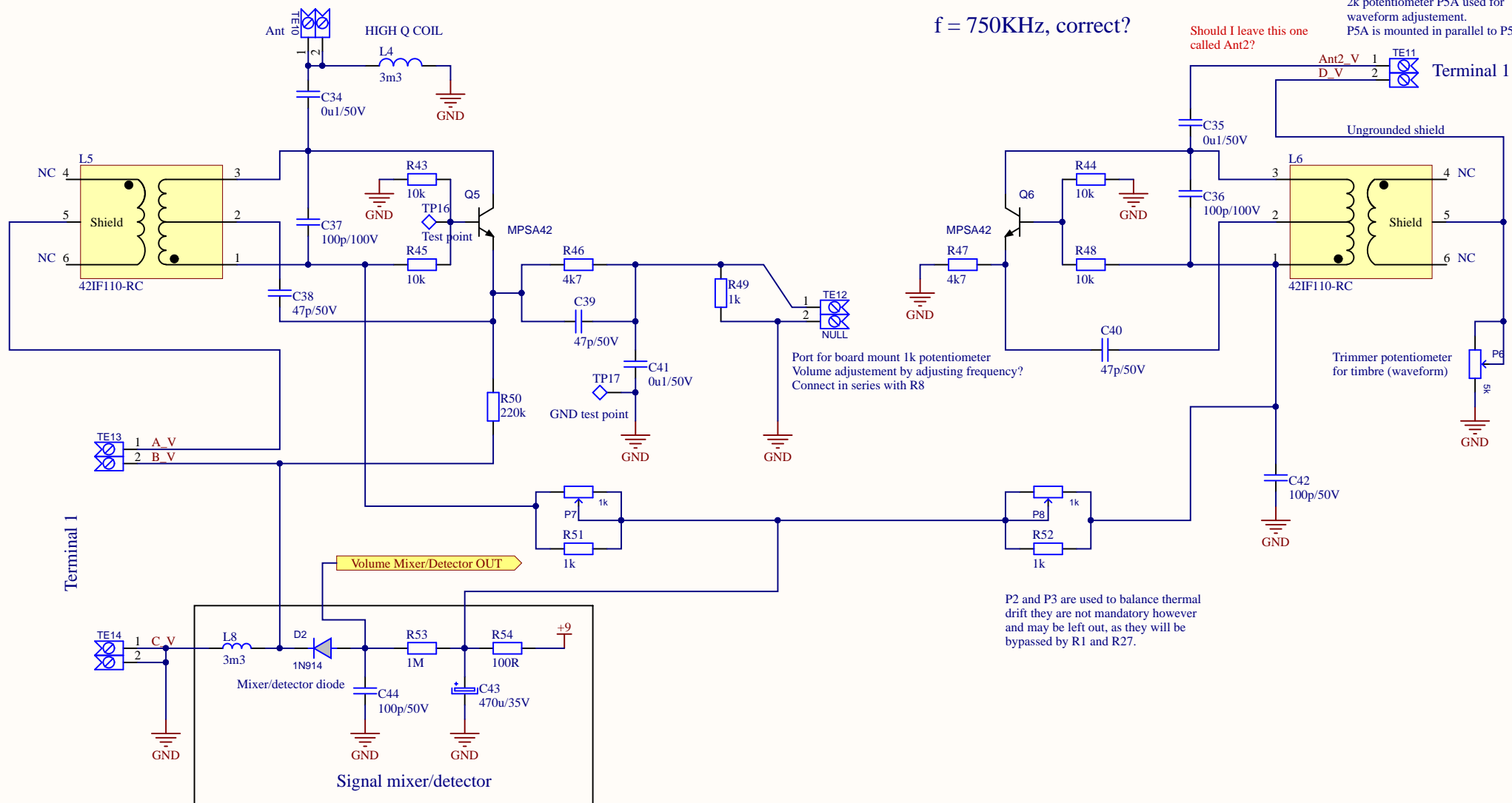
Title		
Phoenix Theremin 2020 - Volume board, Audio section		
Size	Number	Revision
A4	... are both ...	1.07.19A
Date:	21. 01. 2019	Sheet of
File:	H:\FAKS\VolumeBoard - AudioSection	Sheet By:

1

2

3

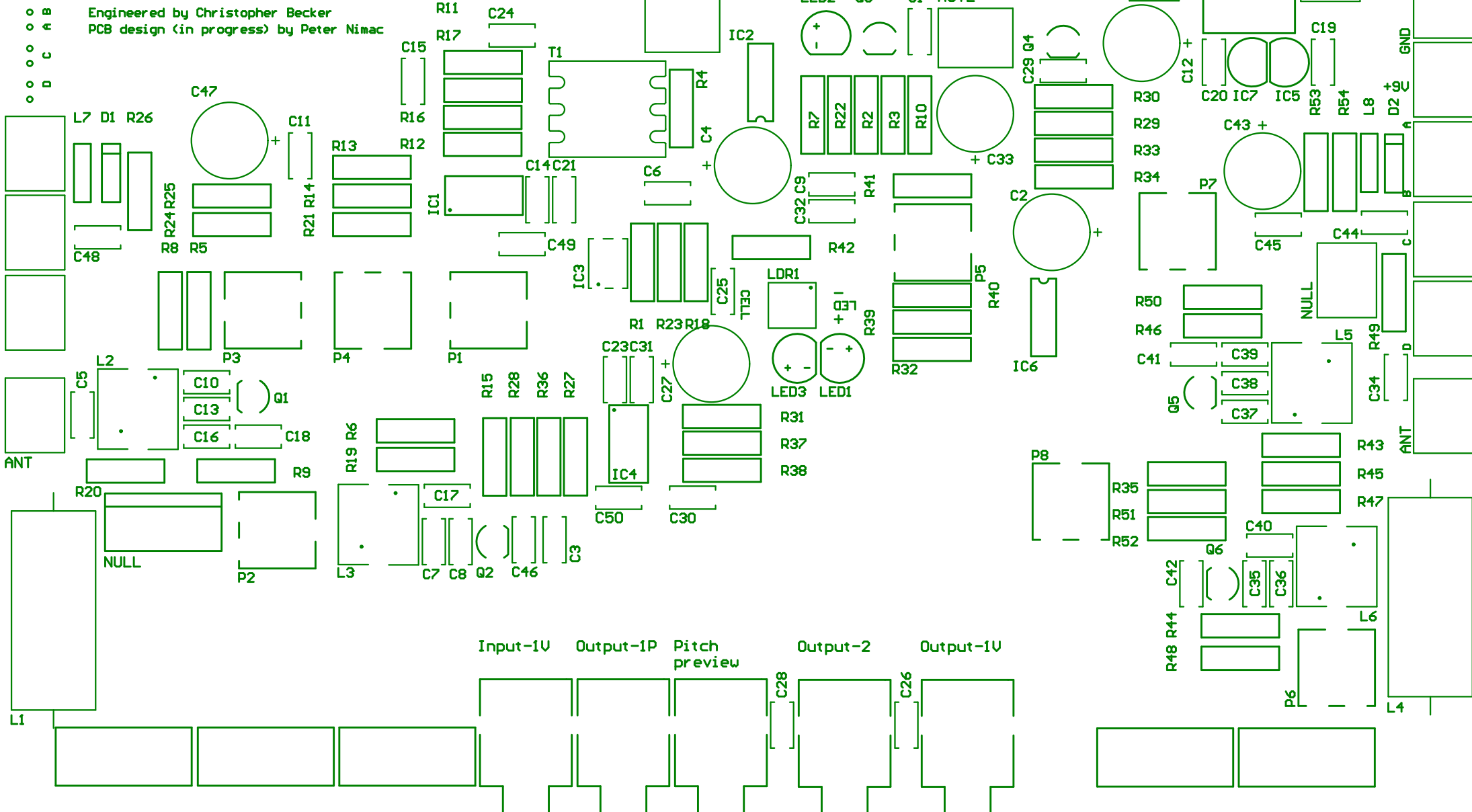
4



Title			
Phoenix Theremin 2020 - Volume board, RF section			
Size	Number	Revision	
A4	... the same thing.	1.07.19A	
Date:	21. 01. 2019	Sheet	of
File:	H:\FAKS\VolumeBoard - RF_Section.SchDoc		

Phoenix theremin 2020
Prototype for a single board revision
(revision B1)

Engineered by Christopher Becker
PCB design (in progress) by Peter Nimac



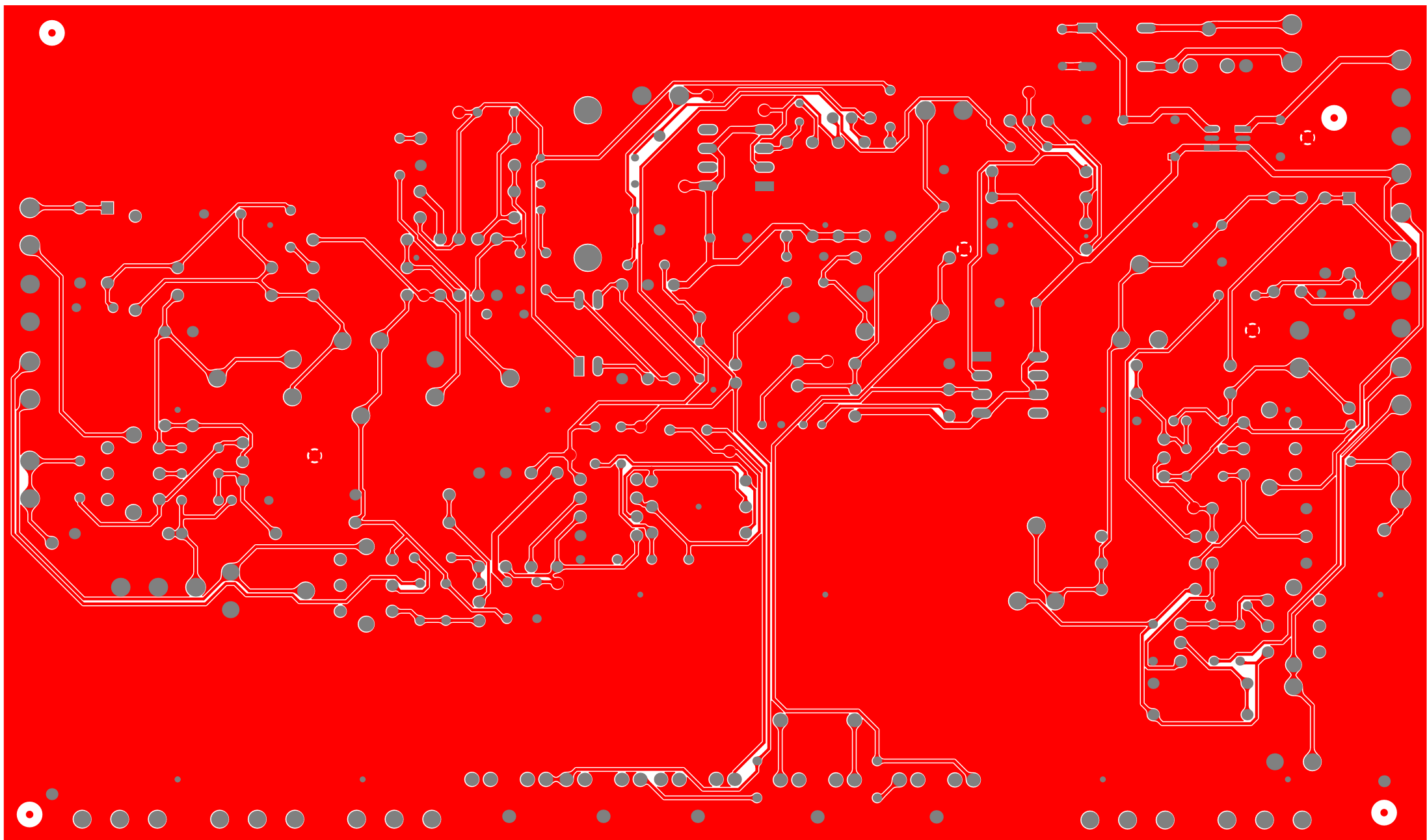
Phone Vol

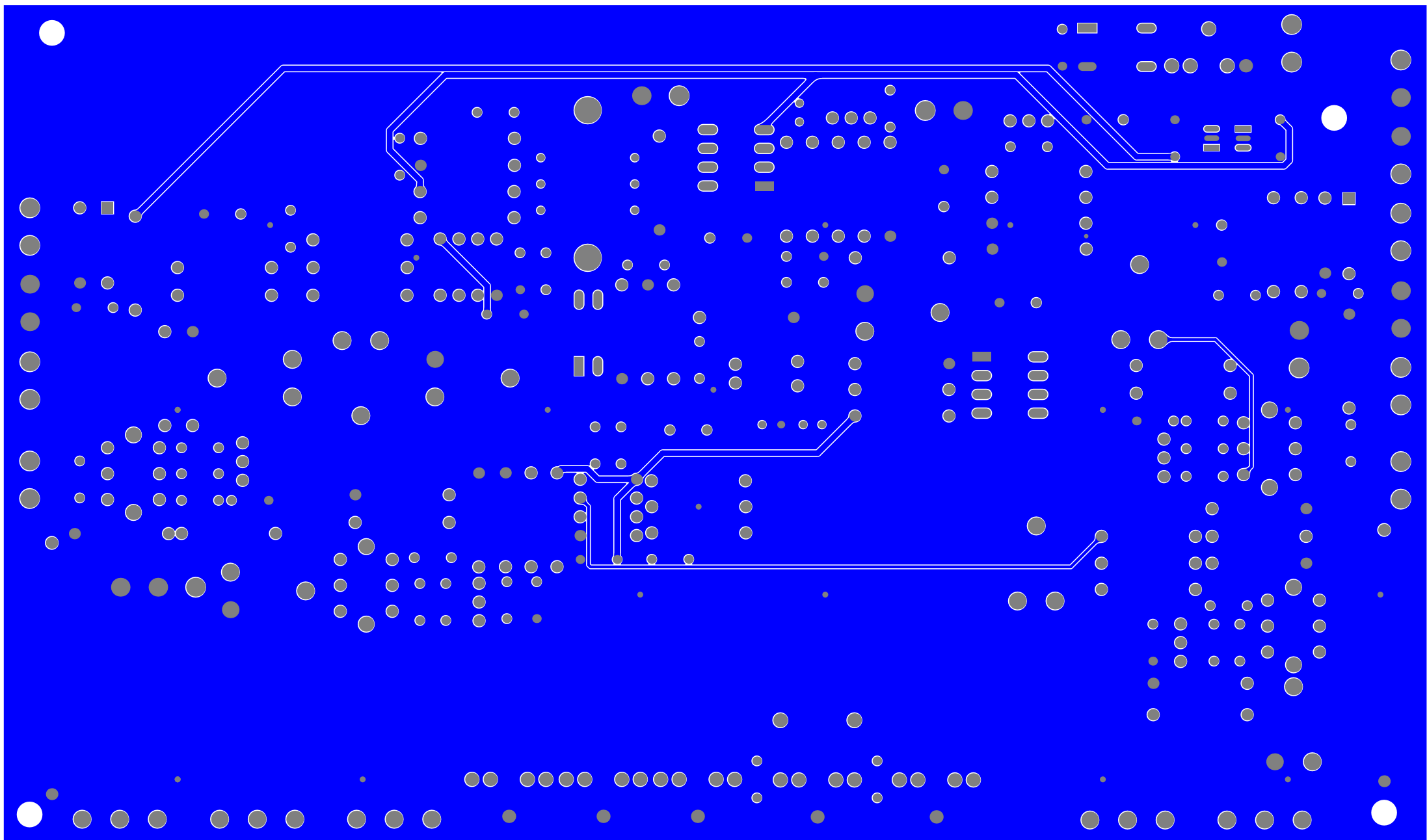
Volume

Pitch

Waveform

Brightness







Volume

Waveform

Brightness